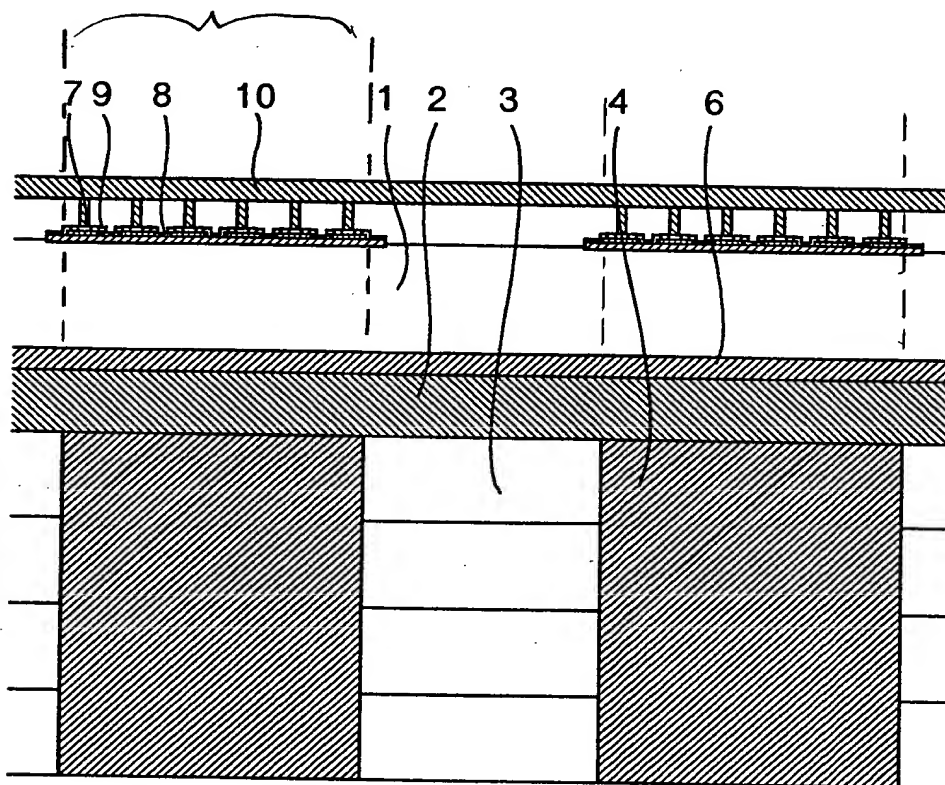




# SKETCH A

FIG.5

All Emitter Electrodes 7  
Located over Thermal Via 4.





# SKETCH B

FIG.8

Peripheral Emitter  
Electrodes 7 NOT Located  
over Thermal Via 4.

